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DELL 595B (Bios Master Key Generator) - DJB3000.rar: 29,94 MB: 26.1. Field of the Invention The present invention generally relates to the field of automatic test equipment (ATE). More specifically, the present invention relates to an ATE test card rack system for handling a plurality of test card rows. 2. Discussion of the Related Art During the test and development of high-speed digital circuits it is often necessary to simulate large numbers of different data streams. Typically these signals are applied in parallel to a number of devices, e.g., digital ASICs (application specific integrated circuits), are monitored. In order to simplify the testing by interconnecting the large number of test cards, it is known to form a test matrix comprising a large number of test cards store that store the store of the devices to be tested. The test matrix of the present invention is designed to handle a number of devices in the range of 64 to 125 test card slots, whereby the number of test card slots may be, for example, 64 to 102. Each test card sack is inserted into a receptacle device, also known as a test card cage, which is also the most convenient way to handle a large number of test card stacks. A test card cage has several fixed slots for receiving a test card stack and several movable slots for locking the respective test card stack in place. The movable and fixed slots may include a number of different features, such as various test pin connectors, test card stack in the lest card stack in the slots, hold-down grippers for lifting the test cards off a test card sage was limited by the number of fixed slots in the test card stack. In the past, the number of test card stacks that could be used with a test card cage was limited by the number of fixed slots in the number of test card stacks that are inserted into the test card cages are relatively expensive, and while they may contain more fixed slots than the number of test card stacks to be inserted, they are not convenient to use. Highly specialized test card cag

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